

Notice of References Cited	Application/Control No. 10/029,699	Applicant(s)/Patent Under Reexamination LUK ET AL.	
	Examiner Kiet T. Ngo	Art Unit 2195	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,138,708	08-1992	Vosbury, Newman A.	714/11
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Rotenberg, Eric, "ArSMT: A Microarchitectural Approach to Fault Tolerance in Microprocessor", 29th Int'l Symp on Fault-Tolerant Computing, Jun 1999, 8 pages
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.